EAST Search History

| Ref # | Hits | Search Query | DBs | Default Operator | Plurals | Time Stamp |
|----------|-------|--|--|---------------------|---------|------------------|
| S14 1 | 12264 | ((over\$1voltage over\$1current surge) and protect\$3).TI. | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/11/16 11:31 |
| S14 2 | 559 | ((over\$1voltage over\$1current surge) and protect\$3 and motor).TI. | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/11/16 11:34 |
| S14 3 | 197 | ((over\$1voltage surge) and protect\$3 and motor).TI. | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/11/16 11:34 |
| S14 4 | 12801 | (over\$1voltage surge transient) same (control processor MCU CPU) same (end stop reduce tur\$1off) | US-PGPUB; USPAT | OR | ON | 2007/11/17 20:12 |
| S14 5 | 1299 | S144 same motor | US-PGPUB; USPAT | OR | ON · | 2007/11/17 20:13 |
| S14 6 | 36 | S145 same protect | US-PGPUB; USPAT | OR | ON | 2007/11/17 20:13 |
| S14 7 | 529 | (361/91.1).CCLS. | USPAT | OR | OFF | 2007/11/19 16:20 |
| S14 8 | 3032 | (318/254).CCLS. | USPAT | OR | OFF | 2007/11/19 16:23 |
| S14 9 | 5434 | (318/254).CCLS. | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB | OR · | OFF | 2007/11/19 16:21 |
| S15 0 | 3090 | S149 and motor and (control protect) | US-PGPUB; USPAT | OR | ON | 2007/11/19 16:22 |
| S15 1 | . 208 | S150 and (overvoltage surge) | US-PGPUB; USPAT | OR | ON | 2007/11/19 16:23 |
| S15 2 | 407 | (318/254).CCLS. | US-PGPUB | OR | OFF | 2007/11/19 16:28 |